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PTO/SB/31 (08-03)
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HOGAN & HARTSON L.L.P.
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SUITE 1900
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| | |
|---|---|
| Firoozeh Vaki Zadeh | (Depositor's name) |
| Firoozeh Vaki Zadeh | (Signature) |
| AUGUST 27, 2004 | (Date) |

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|---|---|---|---|---|
| 10/678,742 | 10/02/2003 | Katsuki Matsudera | 81790.0302 | 6108 |

TITLE OF INVENTION: SEMICONDUCTOR MEMORY DEVICE INPUTTING/OUTPUTTING DATA SYNCHRONOUSLY WITH CLOCK SIGNAL

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|---|---|---|---|---|---|
| nonprovisional | NO | $1330 | $300 | $1630 | 10/20/2004 |

| EXAMINER | ART UNIT | CLASS-SUBCLASS |
|---|---|---|
| YOUNG, BRIAN K | 2819 | 341-100000 |

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
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1 Hogan & Hartson, LLP
2
3

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE: Kabushiki Kaisha Toshiba
(B) RESIDENCE: (CITY and STATE OR COUNTRY): Tokyo, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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- ☐ b. Applicant is not claiming SMALL ENTITY status. See, e.g., 37 CFR 1.27(g)(2).

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(Authorized Signature) [signature] (Date) 8/27/04

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